How to Recruit Quality Analog Students from our GTAC Program

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How?

- Through GTAC Membership
- Through Student Sponsorship

Once a Member, How?

- Establish a Mentor
- Provide Co-ops
- Sample Programs
Become a GTAC Member

- **Why?**
  - Help orient GT’s analog program
  - Access to students
  - Promote analog awareness
  - Increase company visibility (seminar speakers)

- **GT’s Benefits**
  - Train & provide feedback to students
  - Help orient research directions

Sponsor a Student

- **Why?**
  - Develop working relation w/ student - prospective employee
  - Train prospective employee in a specific subject (relatively inexpensive)

- **GT’s Benefits**
  - Promote analog research
  - Fund graduate students
Establish a Mentor

Why?
- Help direct student’s research
- Teach prospective employee practical engineering skills
- Facilitate/expedite program completion
- Foment long-standing relationship with faculty in specific technical areas

Establish a Mentor

GT’s Benefits
- Help orient research
- Expert technical feedback
- Foment long-standing relationship with industry in specific research areas
Establish a Mentor

- **How?**
  - Mentor’s role - provide student with technical feedback on a regular basis (e.g., min: semiannual - based on report via e-mail, fax, or phone-, but up to mentor, maybe an on-site design review)
  - GT’s responsibility – train student, provide status reports, & keep mentor’s load low

Provide Co-ops

- **Why?**
  - Interview student for 4-5 months
  - Expose company to student, and its benefits

- **GT’s Benefits**
  - Train student on practical design skills
    - (not necessarily tied to research but definitely tied to analog!)
  - Student interviews company
Provide Co-ops

- How?
  - 4-5 mo. term - past learning curve to get productive work done & get student fully exposed to product development
  - Keep co-op offers steady, even during bad economic times → maintain company image among students & keep industry/faculty relationships steady

Sample MS Program

- MS Student Timeline
  1. → Spend 1-2 sem. @ GT (take relevant grad. IC design classes & work under faculty’s guidance)
  2. → Identify a mentor (need not be tied to co-op)
  3. → Spend full semester, or more, in a co-op position
  4. → Establish group/student relationship for the remainder of the student’s program
  5. → Spend final 2-3 sem. @ GT
  6. → Final report/paper & ready to go!
Sample Ph.D. Program

- Ph.D. Student Timeline
  1. → Spend 1-2 sem. @ GT
  2. → Identify a mentor (need not be tied to co-op)
  3. → Spend full semester, or more, in a co-op position
  4. → Establish group/student relationship for the remainder of the student’s program
  5. → Spend co-op term # 2, and 1-2 week trips
      -presentations, IC/layout design reviews, etc.-
  6. → Report final results (journal publications) & ready to go!